

## Rakesh M B

Assistant Professor

SIT, Tumkur

Karnataka State

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### EDUCATION

Particulars	Name of the Institution	Board	Aggregate
<b>M.TECH (LVS*) (2012-2014)</b>	J.S.S.Academy of Technical Education , Bangalore	Visvesvaraya Technological University	<b>77.1%</b>
<b>B.E(E&amp;C) (2008)</b>	R.V. College of Engineering Bangalore	Visvesvaraya Technological University	<b>66.1%</b>
<b>II P.U.C (2004)</b>	S.T.J.Pre University College Davangere	Karnataka State PU Board	<b>89.2%</b>
<b>S.S.L.C (2002)</b>	S.T.J.High School Davangere	Karnataka Secondary Education Examination Board	<b>96.48%</b>

\* VLSI Design and Embedded System

### WORK EXPERIENCE

- Assistant Professor at SIT, Tumkur since July 2015 (5 years).
- Assistant Professor at DYP SOEA ,Pune from June 2014 – May 2015 (11 Months).
- Assistant Software Engineer in TCS from 3<sup>1st</sup> October 2008 to 2<sup>3rd</sup> march 2009 (5 Months).

### TECHNICAL SKILLS

- Programming Languages : Verilog, C, ARM Assembly Language.
- Design Tools : Xilinx ISE , LT Spice,  $\mu$  Vision3-Keil.

### CERTIFICATION AND PUBLICATIONS



- Orcid ID : [0000-0002-7819-551X](https://orcid.org/0000-0002-7819-551X)
- Google Scholar ID : [Vjl9JOQAAAAJ](https://scholar.google.com/citations?user=Vjl9JOQAAAAJ)